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THE COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231

Sir:

This a request for an application under 37 CFR 1.53(b) and (f)

Transmitted herewith for filing is the patent application of Inventor(s) Dr. Frank Sattler, et al

For: METHOD FOR MODULATING A BASIC CLOCK SIGNAL FOR DIGITAL CIRCUITS . AND CLOCK MODULATOR FOR IMPLEMENTING THE METHOD

Enclosed are:

- [X] 16 pages, specification and claims; unsigned declaration
- [X] 6 soft 8.5"x11" size sheets of drawings (Figs. 1-6) attached to application
- [X] Filing without fee or Declaration under 37 CFR 1.53(f)
- [X] Express Mail mailing label no. on all filed papers
- [ ] certified copies of a German Patent Application
- [X] Preliminary Amendment (PLEASE ENTER BEFORE CALCULATING CLAIM FEES)
- ·[] Information Disclosure Statement,

## Claims as Filed

		NUMBER FILED	NUMBER EXTRA	RATE	BASIC FEE \$690.00
TOTAL CLAIMS	、12	-20=	0	\$18	0
INDEPENDENT CLAIMS	1	-3=	0	\$78	0
Surcharge fee for filing under 1.53(f) \$130.00					
				·	\$820.00

[X] CLAIM IS HEREBY MADE OF THE BENEFIT OF THE FILING DATE OF THE German Patent Application: 199 33 115.4 filed July 19, 1999 UNDER 35 USC 119.

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